Low Noise InP HFET Receivers

NRAO Experience



Marian W. Pospieszalski Central Development Laboratory

Atacama Large Millimeter/submillimeter Array
Expanded Very Large Array
Robert C. Byrd Green Bank Telescope
Very Long Baseline Array

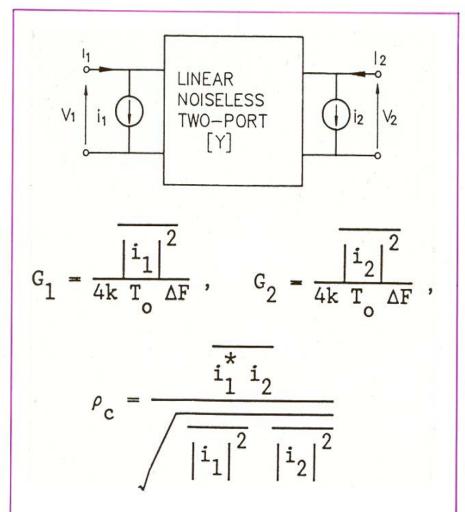


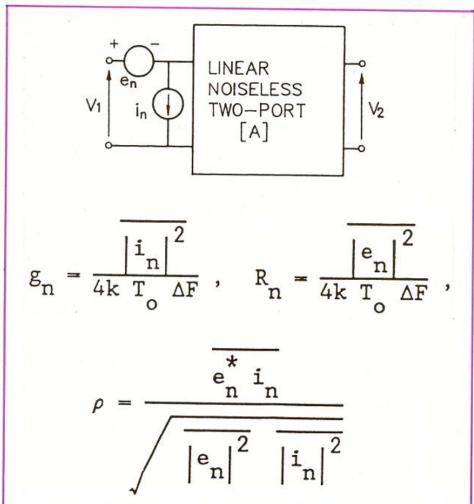
Outline

- Limits on noise parameters of microwave transistors
- Accuracy of model predictions
- Limits on noise performance of InP HFETs
- Practical limits on broadband noise matching: examples
- Other factors limiting noise performance of NRAO receivers
- Examples of performance of VLA receivers
- Expected trade-off in very broad band receiver performance



Noise Representations of 2-Ports







Allowed Values of Noise Parameters (1)

$$T_{n} = T_{min} + 4NT_{o} \frac{\left| \Gamma_{g} - \Gamma_{opt} \right|^{2}}{\left| 1 - \left| \Gamma_{g} \right|^{2}} \qquad \Gamma_{opt} = \frac{Z_{opt} - Z_{o}}{Z_{opt} + Z_{o}}$$

$$N = R_{opt} g_{n}$$

For all linear noisy two-ports for:

$$\left| \rho \right| \le 1$$
 And therefore $\frac{4NT_0}{T_{\min}} \ge 1$

which is equivalent to correlation matrix being Hermitian and non-negative definite.

Allowed Values of Noise Parameters (2)

$$T_{\min} = T_0 \{2 N + Re \left(\rho \sqrt{R_n g_n}\right)\}.$$

If therefore Re(ρ) ≥ 0 and correlation matrix is Hermitian and non-negative definite, than always

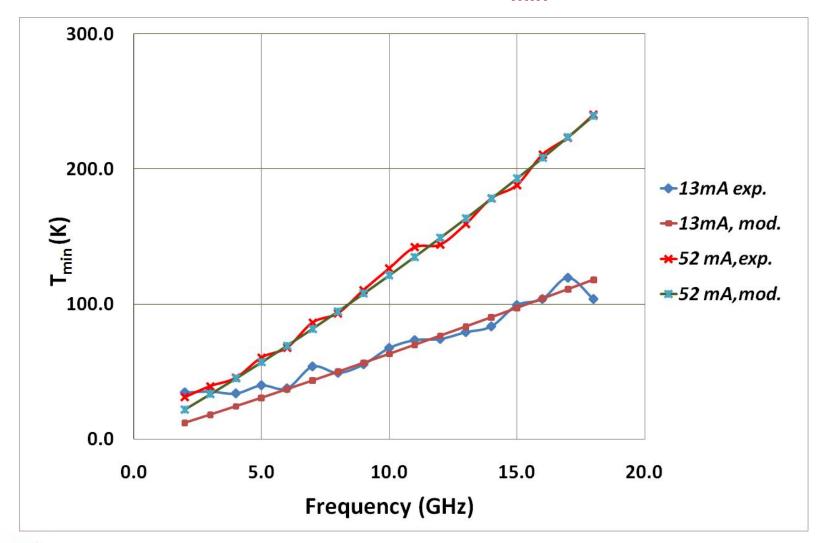
$$1 \le \frac{4 \, \text{NT}_0}{T_{\text{min}}} \le 2$$

For all microwave transistors for useful frequency range:

$$\frac{4NT_0}{T_{\min}} \approx 2$$

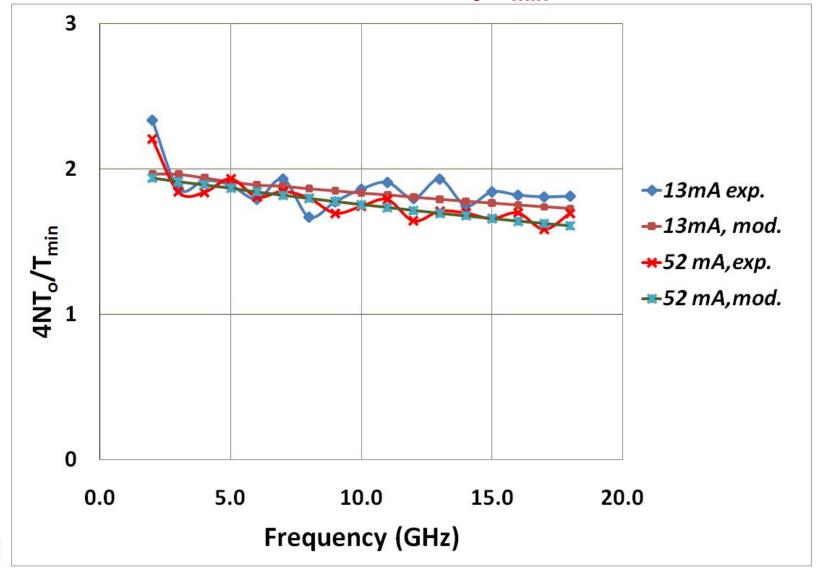


Measured and Modeled T_{min} of a FET

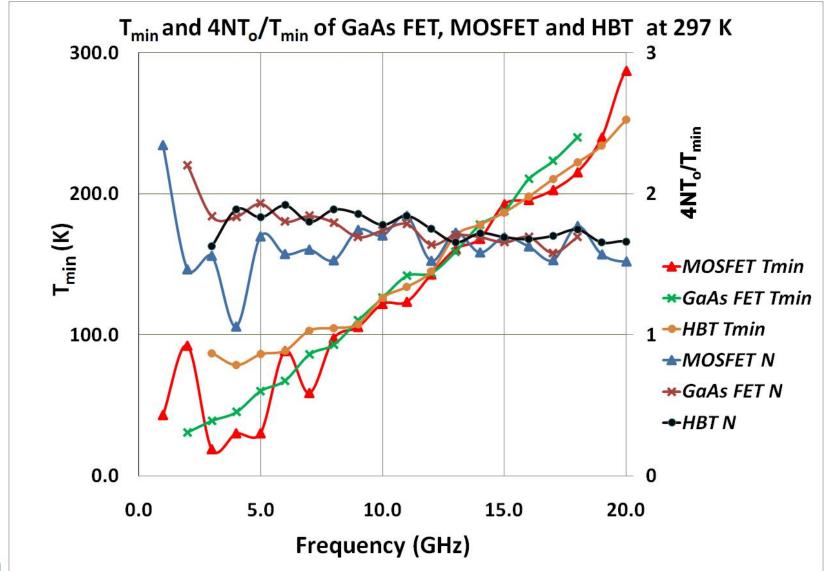




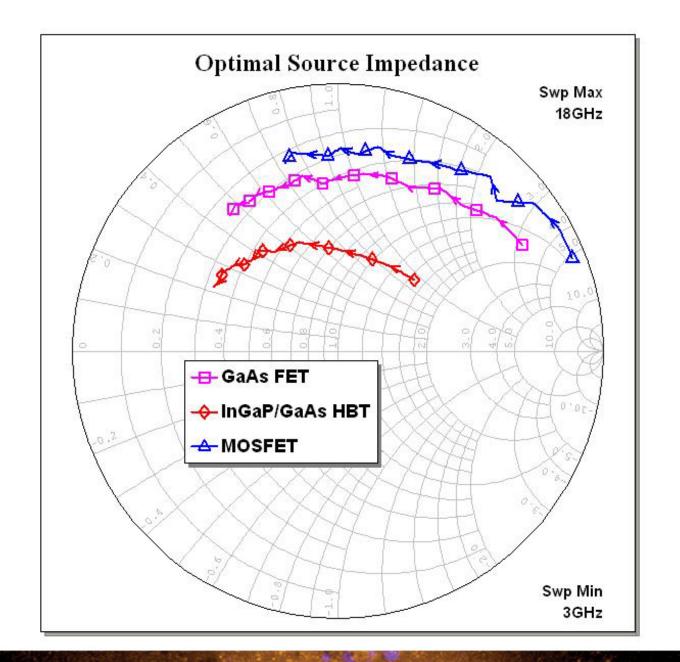
Measured and Modeled 4NT₀/T_{min} of a FET









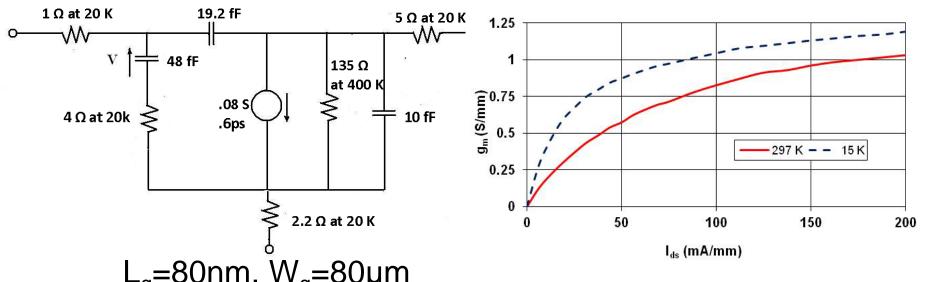




Example of Equivalent Circuit and $g_m(I_{ds})$ **Characteristics of Cryo3 InP HFET**

Equivalent circuit of cryo3 device

g_m(I_{ds}) characteristics

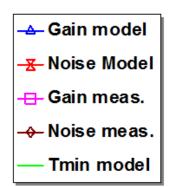


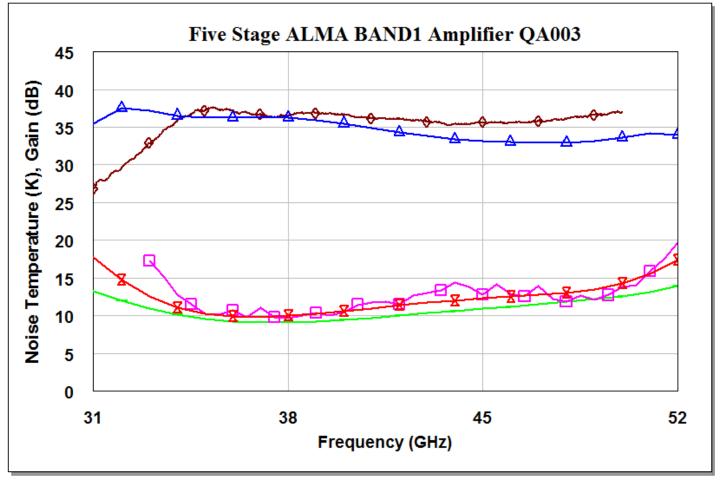
$$L_g=80$$
nm, $W_g=80$ µm

$$T_{\min} \cong 2 \frac{f}{f_t} \sqrt{g_{ds} T_d r_t T_g} \cong \frac{f}{f_{\max}} \sqrt{T_d T_g}$$



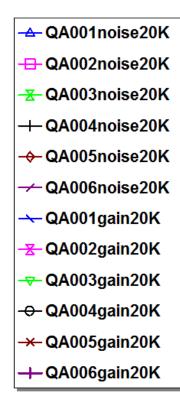
5 stage 33-52 GHz amplifier: model and measured results at 20 K

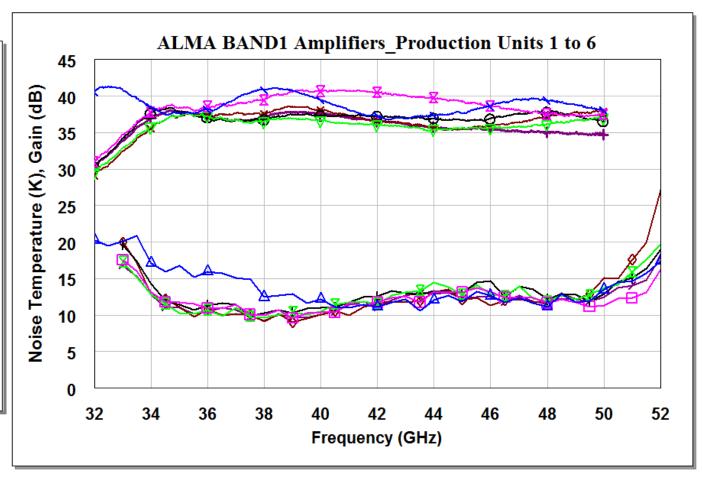






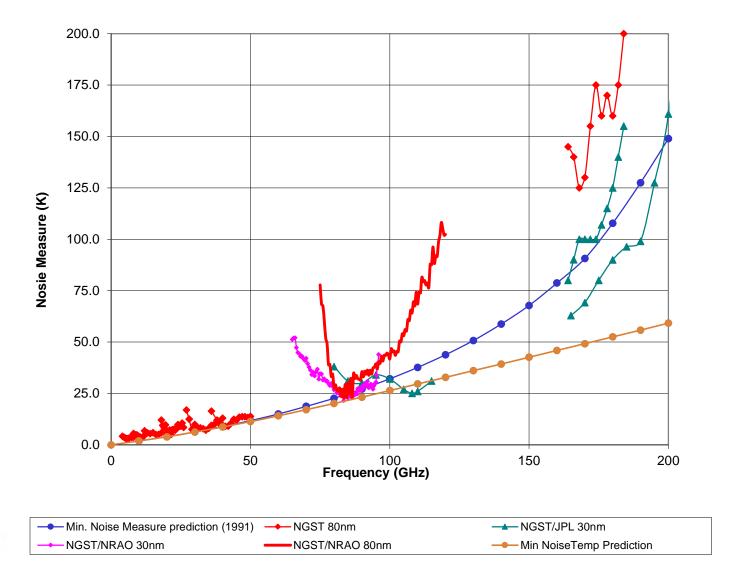
33-52 GHz amplifiers: First Six Units at 20 K







Noise Temperature Summary of Cryogenic HEMTs





Device Scaling: Gate Length (1)

$$T_{\min} \cong 2 \frac{f}{f_t} \sqrt{g_{ds} T_d r_t T_g} \cong \frac{f}{f_{\max}} \sqrt{T_d T_g} \qquad f_t \cong \frac{g_m}{2\pi (C_{gs} + C_{gd})}$$





$$f_{max}$$
 T_{min} if $T_d \approx const.$

$$T_g \cong Ambient temp. T_d$$

depends on gate length, channel structure, and current density I_d/mm but mostly not on ambient temperature

For every device structure there must exist a lower limit on T_{min} upon further device scaling. Exact dependence of T_d on device structure and properties of electron transport in the channel is not known, but



Device Scaling: Gate Length (2)

within a measurement error no device demonstrated $T_{\rm min}$ lower than that predicted in MMA Memo #67 (1991)

The best cryogenic wafers: Chalmers (130 nm), NGSTCryo3 (80-100 nm), NGST (35 nm) exhibit progressively better $\rm f_{max}$ and $\rm M_{min}$ but about the same minimum T_{min} because T_d increases for deep submicron gate lengths.

The reasons:

- Noise optimal bias minimizing the value of : $f(V_{ds}, I_{ds}) \approx \frac{\sqrt{I_{ds}}}{g_{m}}$ increases for (shorts channel effects).
- Also $T_{\mbox{\scriptsize d}}$ increases for the same value of $I_{\mbox{\scriptsize d}}/mm$ (no explanation yet).

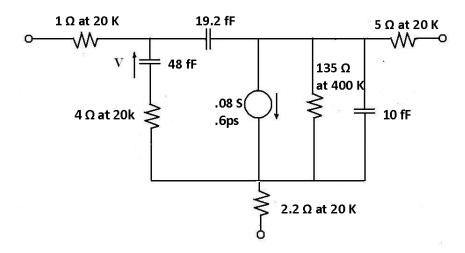
For f_{max} =const. and constant bias:

$$T_{min} \propto \sqrt{T_a}$$



Plausible Explanation?

Equivalent circuit of cryo3 device



 $L_g{=}80nm,\,W_g{=}80\mu m,\,V_{ds}={.}8V,\,I_{ds}{=}3\,\,mA$

Shot noise in the drain?

Assume:

$$4kT_d=2qI_d$$

Than for $I_d=3mA$
 $T_d\approx4700~K$

$$T_{\min} \cong 2 \frac{f}{f_t} \sqrt{g_{ds} T_d r_t T_g} \cong \frac{f}{f_{\max}} \sqrt{T_d T_g}$$



Device Scaling: Gate Width

$$T_{min} \cong 2 \frac{f}{f_t} \sqrt{g_{ds} T_d r_t T_g}$$

$$R_{opt} \cong \frac{f_t}{f} \sqrt{\frac{r_t T_g}{g_{ds} T_d}}$$

$$\mathbf{r}_{\mathrm{t}} = \mathbf{r}_{\mathrm{gs}} + \mathbf{r}_{\mathrm{g}} + \mathbf{r}_{\mathrm{s}}$$







 \mathbf{T}_{\min}



in principle

 T_{\min}



in practice



Important Notes:

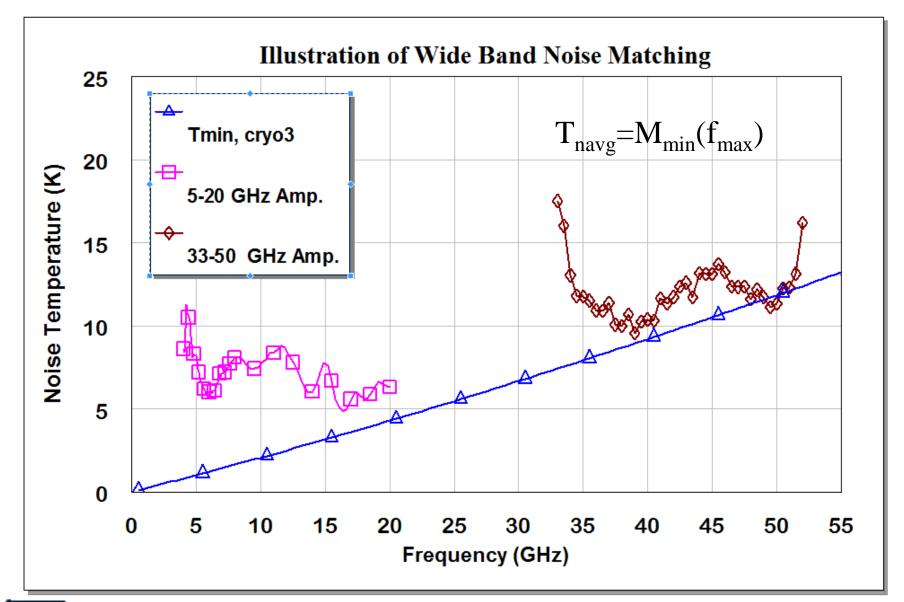
$$T_{n} = T_{\min} + 4NT_{o} \frac{\left|\Gamma_{g} - \Gamma_{opt}\right|^{2}}{\left|1 - \left|\Gamma_{opt}\right|^{2}\right|\left|1 - \left|\Gamma_{g}\right|^{2}} \qquad T_{n} = T_{\min} \frac{\left|\Gamma_{opt}\right|^{2}}{\left|1 - \left|\Gamma_{opt}\right|^{2}\right|} \qquad T_{n} = T_{\min} \frac{\left|\Gamma_{opt}\right|^{2}}{\left|1 - \left|\Gamma_{opt}\right|^{2}\right|}$$

For a given frequency range chose Γ_{opt} close to SC center.

In practice, for a given frequency range average T_n is:

$$\frac{1}{f_{\text{max}} - f_{\text{min}}} \int_{f_{\text{min}}}^{f_{\text{max}}} T_{n} df \approx M_{\text{min}}(f_{\text{max}})$$









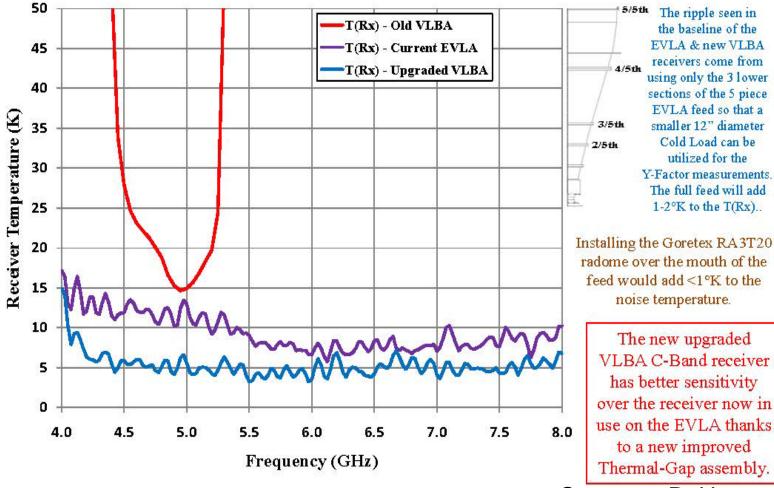
JVLA K_a- Band Receiver

Courtesy: R. Hayward,

P. Harden, NRAO



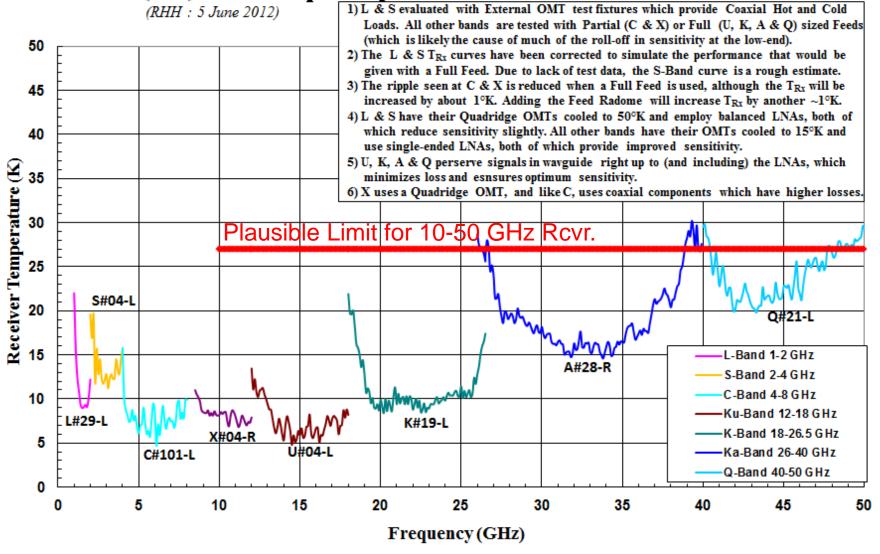
Prototype VLBA C-Band (S/N 11) Sensitivity Old VLBA vs. New VLBA vs. EVLA Receivers





Courtesy: R. Hayward

T(Rx) vs. Frequency for JVLA Receiver Bands





Courtesy: R. Hayward

Summary

- Only three wafer runs of InP discrete devices (NRAO/HRL, WMAP/HRL, NGST/JPL cryo3) have been used in construction of great majority of radio astronomy instruments: VLA/EVLA,VLBA, GBT, ALMA band6, CBI, SZ-Array, WMAP, Planck LFI (K_a and Q), VSA, AMI, MPI, JPL/DSN and others
- No single wafer devices have ever been fully understood
- There has been no significant progress in the low noise performance of cryogenic HFET's in the past 15 years; Are we approaching the limits?
- Amplifier noise temperature is no longer the dominant component of the system noise for radio astronomy instruments with cryogenic receivers
- Very broadband receivers will suffer loss of sensitivity which can be well predicted, with accuracy satisfactory for the assessment of possible benefits

